# III B. Tech I Semester Supplementary Examinations, May - 2019 <br> DIGITAL COMMUNICATIONS <br> (Electronics and Communication Engineering) 

Time: 3 hours
Max. Marks: 70

Note: 1. Question Paper consists of two parts (Part-A and Part-B)<br>2. Answer ALL the question in Part-A<br>3. Answer any FOUR Questions from Part-B

## PART -A

1. a) Discuss about the different noise effects in Delta Modulation.
b) Explain the non-coherent detection of binary FSK signals.
c) Compare a correlator and matched filter.
d) Calculate the amount of information if binary digits occur with equal likelihood in [3M] binary PCM systems.
e) What is discrete memory less channels?
f) Explain about BCH codes.

## PART -B

2. a) Explain quantization error and derive an expression for maximum SNR in PCM [7M] system that uses linear quantization.
b) In a binary PCM system, the output signal to quantizing noise ratio is to be held to a minimum value of 40 dB . Determine the number of levels and find the corresponding signal to quantizing noise ratio.
3. a) Explain how integrator is used to detect the baseband signal. Obtain an expression for $\mathrm{S} / \mathrm{N}$ of integrator and dump receiver.
b) Obtain the probability of error for Matched filter.
4. a) Draw and explain the coherent system of signal reception.
b) Binary data is transmitted over a telephone line with usable bandwidth of 2400 Hz using the FSK signaling scheme. The transmit frequencies are 2025 and 2225 Hz , and the data rate is $300 \mathrm{bits} / \mathrm{Sec}$. The average signal to noise power ratio at the output of the channel is 6 dB . Calculate Probability of error for the coherent and non coherent demodulation schemes.
5. a) Explain the concept of entropy and its properties.
b) An analog signal band limited to 10 kHz is quantized in 8 levels of a PCM system with probabilities of $1 / 4,1 / 5,1 / 5,1 / 10,1 / 10,1 / 20,1 / 20$ and $1 / 20$ respectively. Calculate the entropy and the rate of information.
6. a) Discuss in brief about continuous channel capacity.
b) Calculate the capacity of the discrete channel shown in Fig.1. Assume [7M] $\mathrm{r}_{\mathrm{s}}=1 \mathrm{symbol} / \mathrm{sec}$,

7. a) Explain sequential decoding for convolutional codes.
b) Draw the state diagram, tree diagram, and trellis diagram for $\mathrm{k}=3$, rate $1 / 3$ code generated by $g_{1}(x)=1+x^{2}, g_{2}(x)=1+x$ and $g_{3}(x)=1+x+x^{2}$.
