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Bachelor of Science (B.Sc.) Semester-III (C.B.S.) Examination **ELECTRONICS** (OP-AMP & POWER SUPPLY) Paper-I Time : Three Hours] [Maximum Marks : 50 **N.B.** :— (1) All questions are compulsory and carry equal marks. (2) Draw neat diagrams wherever necessary. (3) Only 10 out of 12 sub-questions are to be attempted from Question No. 5. **EITHER** (A) Draw the circuit of emitter coupled difference amplifier and explain its working in common and differential modes. What is the need for two power supplies in a difference amplifier ? OR (B) Draw the block diagram of OP-Amp and explain each block. State the basic characteristics of ideal OP-Amp. **EITHER** (A) Explain the operation of OP-Amp as inverting amplifier. If the adder has $R_f = 10 \text{ k}\Omega$, $R_1 = 10 \text{ k}\Omega$, $R_2 = 2.2 \text{ k}\Omega$, $R_3 = 3.3 \text{ k}\Omega$, $V_1 = 6V$, $V_2 = -3V$, $V_2 = -0.75$ V. What is its output voltage ? OR (B) Explain the operation of OP-Amp as integrator. State its limitations. 5 + 2Explain OP-Amp integrator output for the following inputs : Square wave (i) Sine wave (ii) (iii) DC. EITHER (A) Give the working of Bridge wave rectifier and state its advantages. Explain the need of filter in a power supply. Give the working of capacitive filter. OR (B) Differentiate between unregulated and regulated power supply. Explain the Zener diode as a voltage regulator and state its advantages. Explain line and load regulation. 2+5+3

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EITHER

4.	(A)	What are the general features of I_c regulator ? Explain in brief (any five).	5
		Design and explain the working of a variable power supply using LM 317 PC.	5
	OR	835	
	(B)	Draw the block diagram of Switched Mode Power Supply and explain its working.	6
		What is the concept of LDO ? Explain. State its advantages.	4
5.	Answer any TEN :—		
	(a)	Draw the symbol of OP-Amp.	
	(b)	State the limitations of DC amplifier.	
	(c)	What is CMRR ?	
	(d)	Draw the circuit diagram of voltage follower.	
	(e)	State the limitation of Integrator.	
	(f)	Explain how comparator will work as zero crossing detector.	
	(g)	What is rectifier ?	
	(h)	State the limitations of zener regulator.	
	(i)	Draw the circuit for +5V power supply using I_c regulator.	
	(j)	What is voltage stability factor ?	
	(k)	State the principle of LDO.	
	(1)	State any two applications of SMPS.	1×10

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